



Patent / Docket No. 24061.39
Customer No. 27683

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Jhon Jhy Liaw

Serial No. 10/687424

Filed: October 31, 2003

For: PROCESS INTEGRATION OF SOI
FETS WITH ACTIVE LAYER SPACER

§
§ Group Art Unit: Unknown
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§
§ Examiner: Not Assigned
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§
§

TRANSMITTAL

Mail Stop Patent Application
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed are the following regarding the above-identified patent application:

1. Power of Attorney for Patent Application; and
2. return postcard.

The Commissioner is hereby authorized to charge payment of any further fees associated with any of the papers submitted herewith or to credit any overpayment to Deposit Account No. 08-1394.

Respectfully submitted,

David M. O'Dell
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Date: 12-19-03

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R-62198

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Jhon Jhy Liaw

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Attorney Docket No.: 24061.39
TSMC2002-0030

Serial No.: Unknown

Customer No. 27683

Filed: Herewith

Group Art Unit: Unknown

For: A METHOD OF MANUFACTURING
A MICROELECTRONICS DEVICE
WITH ACTIVE LAYER SPACER

Examiner: n/a

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

POWER OF ATTORNEY FOR PATENT APPLICATION

As a representative of the Assignee, Taiwan Semiconductor Manufacturing, Co. Ltd., I hereby appoint the following attorneys and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

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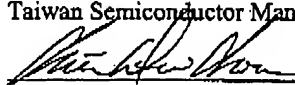
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The undersigned is the representative for the Assignee of the entire right, title, and interest in the patent application identified above. A copy of the assignment or other documents in the chain of title are attached.

The undersigned (whose title is supplied below) is authorized to act on behalf of the Assignee.

Taiwan Semiconductor Manufacturing, Company, Ltd

12-10-03
Date


Chien-Wei Chou
Title: Director, IP Division